



SUB
E07
D2

7. (Amended) A multiple display supporting module comprises:
a processing module; and
memory operably coupled to the processing module, wherein the memory includes operational instructions that cause the processing module to (a) receive capability parameters regarding a first display of the multiple displays, wherein the capability parameters comprise display resolution and display pixel depth; (b) substitute selected display capabilities for the capability of parameters; and (c) provide the selected display capabilities to an operating system.

D3

13. (Amended) A digital storage medium for storing operational instructions that cause a processing module to support multiple displays associated with a drawing surface, the digital storage medium comprises:
first storage means for storing operational instructions that cause the processing module to receive capability parameters regarding a first display of the multiple displays, wherein the capability parameters comprise display resolution and display pixel depth;
second storage means for storing operational instructions that cause the processing module to substitute selected display capabilities for the capability parameters; and
third storage means for storing operational instructions that cause the processing module to provide the selected display capabilities to an operating system.

SUB
E0
D4

19. (Amended) A method for supporting multiple displays per drawing surface, the method comprises the steps of:
a) receiving capability parameters for each display of the multiple displays, wherein the capability parameters comprise display resolution and display pixel depth;
b) determining selected display capabilities based on the capability parameters of each display of the multiple display;
c) Substituting the selected display capabilities for the capability parameters of a respective display of the multiple displays; and
d) providing the selected display capabilities to an operating system.

SUB E9
D5
cont

23. (Amended) A multiple display supporting module comprises:
a processing module; and

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memory operably coupled to the processing module, wherein the memory includes operational instructions that cause the processing module to execute the steps of

(a) receiving capability parameters for each display of the multiple displays, wherein the capability parameters comprise display resolution and display pixel depth;

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(b) determining selected display capabilities based on the capability parameters of each display of the multiple displays;

(c) substituting the selected display capabilities for the capability parameters of a respective display of the multiple displays; and

(d) providing the selected display capabilities to an operating system.

27. (Amended) The method of claim 1 wherein the capability parameters further comprise a display refresh rate.

D6

28. (Amended) The method of claim 7 wherein the capability parameters further comprise a display refresh rate.

29. (Amended) The method of claim 13 wherein the capability parameters further comprise a display refresh rate.

30. (Amended) The method of claim 19 wherein the capability parameters further comprise a display refresh rate.

31. (Amended) The method of claim 23 wherein the capability parameters further comprise a display refresh rate.

REMARKS

Claims 1-26 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Butler, et al. (U.S. Patent No. 6,018,340) in view of Ishikura, et al. (U.S. Patent No. 5,585,821) in further view of Dye (U.S. Patent No. 6,108,014). Claims 27-31 were not addressed in the Office Action.